

Practical Low Power Digital Vlsi Design By Gary K Yeap Download

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low power digital vlsi design circuits and systems addresses both process technologies and device modeling power dissipation in cmos circuits several practical circuit examples and low power techniques are discussed low voltage issues for digital cmos and bicmos circuits are emphasized the book also provides an extensive study of advanced cmos subsystem design a low power design methodology is presented with various power minimization techniques at the circuit logic architecture and algorithm levels features low voltage cmos device modeling technology files design rules switching activity concept low power guidelines to engineering practice pass transistor logic families power dissipation of i o circuits multi and low vt cmos logic static power reduction circuit techniques state of the art design of low voltage bicmos and cmos circuits low power techniques in cmos srams and drams low power on chip voltage down converter design numerous advanced cmos subsystems e g adders multipliers data path memories regular structures phase locked loops with several design options trading power delay and area low power design methodology power estimation techniques power reduction techniques at the logic architecture and algorithm levels more than 190 circuits explained at the transistor level

power consumption has become a major design consideration for battery operated portable systems as well as high performance desktop systems strict limitations on power dissipation must be met by the designer while still meeting ever higher computational requirements a comprehensive approach is thus

required at all levels of system design ranging from algorithms and architectures to the logic styles and the underlying technology potentially one of the most important techniques involves combining architecture optimization with voltage scaling allowing a trade off between silicon area and low power operation architectural optimization enables supply voltages of the order of 1 v using standard cmos technology several techniques can also be used to minimize the switched capacitance including representation optimizing signal correlations minimizing spurious transitions optimizing sequencing of operations activity driven power down etc the high efficiency of dc dc converter circuitry required for efficient low voltage and low current level operation is described by stratakos sullivan and sanders the application of various low power techniques to a chip set for multimedia applications shows that orders of magnitude reduction in power consumption is possible the book also features an analysis by professor meindl of the fundamental limits of power consumption achievable at all levels of the design hierarchy svensson of isi describes emerging adiabatic switching techniques that can break the CV^2f barrier and reduce the energy per computation at a fixed voltage srivastava of at t presents the application of aggressive shut down techniques to microprocessor applications

this book provides for the first time a broad and deep treatment of the fields of both ultra low power electronics and bioelectronics it discusses fundamental principles and circuits for ultra low power electronic design and their applications in biomedical systems it also discusses how ultra energy efficient cellular and neural systems in biology can inspire revolutionary low power architectures in mixed signal and rf electronics the book presents a unique unifying view of ultra low power analog and digital electronics and emphasizes the use of the ultra energy efficient subthreshold regime of transistor operation in both chapters on batteries energy harvesting and the future of energy provide an understanding of fundamental relationships between energy use and energy generation at small scales and at large scales a wealth of insights and examples from brain implants cochlear implants bio molecular sensing cardiac devices and bio inspired systems make the book useful and engaging for students and practicing engineers

this collection of important papers provides a comprehensive overview of low power system design from component technologies and circuits to architecture system design and cad techniques low power cmos design summarizes the key low power contributions through papers written by experts in this evolving field

interest in indoor wireless communications has been increasing in addition to high throughput wlan systems such as 802.11a/b/g/n attention is also being focused on lower rate short distance systems such as bluetooth and zigbee these low rate radios are being proposed for a variety of applications including automation security smart toys remote sensing control asset tracking and as a replacement for computer peripheral wires while not demanding aggressive throughput these radios do require low cost power efficient operation and optionally the ability to perform ranging unfortunately currently reported radios are up to an order of magnitude away from these power and cost targets or do not support ranging however a recent ruling from the fcc has opened up nearly 8ghz of unlicensed spectrum from dc to 960mhz and from 3.1ghz to 10.6ghz for ultra wideband uwb deployment one attractive method of uwb signaling that seems suited to a low power highly integrated implementation communicates with short pulses on the order of a nanosecond that spread energy over at least 500mhz of bandwidth termed impulse uwb the baseband nature of this signaling promises low cost and low power consumption through design simplicity pulsed or duty

cycled operation and a mostly digital implementation the benefits of this approach are balanced by the risk of jamming from in band interference of stricter sampling and gain constraints and of increased digital complexity this dissertation presents the system exploration specification design and demonstration of a low power highly integrated flexible baseband impulse ultra wideband transceiver front end

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